

Preliminary Amendment  
Application No.: filed concurrently  
October 19, 2005

AMENDMENTS TO THE SPECIFICATION

Please substitute the paragraph beginning at page 19, line 19 and ending at page 20, line 2 to read as follows:

The host information memory 142 stores an access speed transferred by the access unit 100, and either state of a high speed mode or a low speed mode is stored. When the high speed mode is set, since the nonvolatile memory access section 144 permits access with a large peak current, access at high speed is performed upon accessing the nonvolatile memory [[1117]] 117. Further, when the low speed mode is set, upon accessing the nonvolatile memory 117, it is controlled so that the load on the power supply is alleviated by decreasing the peak current.